

SOI Silicon Valley Symposium

April 13 - 2017

Hyatt Regency, Santa Clara, CA



08.00 - 09.00a.m. REGISTRATION

09.00 - 09.05a.m. WELCOME
Carlos Mazure, Executive Director, SOI Industry Consortium

09.05 - 10.50a.m. SOI APPLICATIONS AND PERSPECTIVES

09.05 - 09.45a.m. **Application of Technology in Embedded Processing**
Geoff Lees, SVP & GM, Microcontrollers, NXP

09.45 - 10.25a.m. **FDX™ (FD-SOI) Accelerating Boosting Product Competitiveness in High Growth Markets**
Alain Mutricy, SVP Product Management, GLOBALFOUNDRIES

10.25 - 10.50a.m. Steve Chu, CSO, Huawei

10.50 - 11.10a.m. NETWORKING BREAK

11.10 - 12.40p.m. IOT AND MARKET OPPORTUNITIES

11.10 - 11.50a.m. **Next Steps in IoT's Evolution**
James Stansberry, SVP & US Head of ARTIK Samsung IoT, Samsung

11.50 - 12.15p.m. **Low Power IP: Essential Ingredients for IoT Opportunities**
Ron Moore, VP Physical Design Group, ARM

12.15 - 12.40p.m. **Growth Areas for IoT and the Impact on FD SOI**
Handel Jones, CEO, IBS

12.40 - 01.30p.m. LUNCH

ORGANIZER



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SAMSUNG

AFTERNOON SESSION

01.30 - 03.30p.m. IP ECOSYSTEM AND PRODUCT INNOVATIONS

- 01.30 - 02.00p.m. **FD-SOI In The Connected World**
John Koeter, VP, Marketing Solutions Group, Synopsys
- 02.00 - 02.30p.m. **IoT unique identity overlooked**
Maurizio Paganini, CMO, Megachips
- 02.30 - 02.50p.m. **A new Computer Vision Processor Chip Design for automotive ADAS CNN applications in 20nm FDSOI**
Jens Benndorf, COO, Dreamchip
- 02.50 - 03.10p.m. **High performance ultra low power parallel IoT processor architecture: leveraging FD-SOI**
Loic Lietar, CEO, Greenwaves
- 03.10 - 03.30p.m. **FDSOI factors of success pave new roadmaps**
Marie-Noelle Semeria, CEO, CEA-Leti

03.30 - 04.00p.m. NETWORKING BREAK

04.00 - 05.30p.m. PANEL - DESIGN-TECHNOLOGY ECOSYSTEM READINESS

Moderator: Adele Hars, SOI Consortium / ASN

Panelists: Wayne Dai, CEO, VeriSilicon
Samuel George, Sr. Director, GLOBALFOUNDRIES
John Koeter, VP, Marketing Solutions, Synopsys
Jayanta Lahiri, VP, Invecas
Kelvin Low, Sr. Director, Samsung
Christophe Maleville, EVP &GM, Digital, Soitec
Ron Moore, VP Physical Design Group, ARM
Samir Patel, CEO, Sankalp Semiconductor

05.30 - 05.40p.m. **CLOSING REMARKS**
Carlos Mazure, Executive Director, SOI Industry Consortium