

The 5th Shanghai FD-SOI Forum

September 26, 2017

Pudong Kerry Hotel Shanghai

Morning Agenda

08:00 – 09:00 Registration

Session 1: Opening Keynote

Chair: Dr. Wayne Dai, Chairman, President and CEO, VeriSilicon

09:00 – 09:10 **Welcome**

Dr. Carlos Mazure, Chairman & Executive Director, SOI Industry Consortium

09:10 - 09:25 **Remarks**

Dr. Xi Wang, Academician of Chinese Academy of Sciences, Director General of Shanghai Institute of Microsystem and Information Technology, Chinese Academy of Sciences

09:25 - 09:40 **Remarks**

Liedong Liu, Deputy Mayor, Chengdu Municipal People's Government

09:40 - 10:15 **Winning with SOI**

Dr. Sanjay Jha, CEO, GlobalFoundries

10:15 - 10:50 **Cutting-edge Technologies from a Trusted Foundry**

Dr. ES Jung, EVP & GM, Foundry Business, Samsung Electronics

10:50 - 11:20 **Networking Break** Sponsored by Samsung **SAMSUNG**

Session 2: FD-SOI Deployment

Chair: Dr. Wayne Dai, Chairman, President and CEO, VeriSilicon

11:20 - 11:40 **Advancement in Edge Computing with HMI Interfaces, Artificial Intelligence and FD-SOI**

Ron Martino, VP & GM of i.MX Application Processor Product Line, NXP

11:40 - 12:05 **FD-SOI Opportunities in Deep Learning and Artificial Intelligence Applications**

Dr. Handel H. Jones, CEO, IBS

12:05 - 12:30 **NSIG Support of SOI Ecosystem**

Nabeel Gareeb, CEO, NSIG

12:30 - 14:00 **Lunch** Sponsored by GlobalFoundries  **GLOBALFOUNDRIES** 

Afternoon Agenda

Session 3: FD-SOI Ecosystem

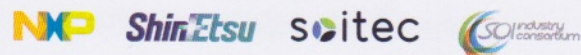
Chair: Dr. Carlos Mazure, Chairman & Executive Director, SOI Industry Consortium

- 14:00 – 14:20 **Accelerating SOI Mass Adoption**
Paul Boudre, CEO, Soitec
- 14:20 – 14:40 **Benefits of 22FDX for NB-IOT RF Integration**
Tom McKay, Director, RF Innovation, RF Pathfinding, Santa Clara, GlobalFoundries
- 14:40 - 15:00 **Energy Efficient RISC-V Processor Design Using 28 FDS**
Borivoje Nikolic, Professor of EECS, UC Berkeley
- 15:00 - 15:20 **SoC Product Design with Body Biasing Using FD-SOI**
Jiong Zhu, Director of Design Implementation, VeriSilicon
- 15:20 - 15:40 **FD-SOI Foundry Enablement: From Concept to Mass Production**
Dr. Qi Wang, VP & Chief of Staff to the CEO, Cadence
- 15:40 - 16:00 **Design with FD-SOI: Innovation Through Collaboration**
Swami Venkat, Senior Director of Design Group, Synopsys
- 16:00 - 16:30 **Networking Break Sponsored by Samsung SAMSUNG**

Session 4: Panel on FD-SOI Design and Applications

Moderator: Dr. Wayne Dai, Chairman, President and CEO, VeriSilicon

- 16:30 - 17:30 **Panelists:**
Ron Martino, VP & GM, NXP
Tom McKay, Director, RF Innovation, RF Pathfinding, Santa Clara, GlobalFoundries
Borivoje Nikolic, Professor of EECS, UC Berkeley
Marshal Cheng, VP, Leadcore
Lei Shen, VP, Fudan Microelectronics
Rajiv Jain, VP of Operations, QuickLogic
David Dutton, CEO, Silvaco
Kelvin Low, VP of Marketing, Physical Design Group, ARM
- 17:30 - 17:40 **Closing Remarks and Announcements**
Giorgio Cesana, Executive Co-Director, SOI Industry Consortium
- 18:30 - 21:00 **Evening Reception**
Dinner Cruise on Huangpu River
Sponsored by NXP, Shin-Etsu Handotai, Soitec, SOI Consortium



Organizers and Sponsors

