



Nitin Chawla, STMicroelectronics, Director

Speaker Bio(Short)

Nitin Chawla is a Senior Principal Scientist in the Technology and Design Platform Group at STMicroelectronics. Nitin's current area of work includes transversal projects primarily in the field of Low Power Neural computing architectures for Embedded Systems, Ultra Low Power Signal Processing and Digitally Assisted SOC Infrastructure/Connectivity IP's. Before joining STMicroelectronics he was the Chief Scientist for the High Level Synthesis Product Group at Mentor Graphics corporation.

Nitin has 9 Granted and several pending US patents. He has published at several top-tier IEEE conferences and journals, including a Best Paper Award in the area of SOC Design.