

SOI Silicon Valley Symposium

April 26 - 2018

Hyatt Regency, Santa Clara, CA



08.00 - 09.00a.m.	REGISTRATION
09.00 - 10.15a.m.	WELCOME AND KEYNOTES
09.00 - 09.15	Welcome, Carlos Mazure, Chairman & Executive Director, SOI Industry Consortium
09.15 - 09.45	Keynote: Get More With Less in AI & IoT Era Frankwell Jyh-Ming Lin, CEO of Andes Technology
09.45 - 10.15	Keynote: The Future of Embedded Processing with FD SOI Ron Martino, VP & GM, NXP
10.15 - 10.45a.m.	NETWORKING BREAK
10.45 - 12.15p.m.	PRODUCTS AND OPPORTUNITIES Chair: C. Mazure, SOI Industry Consortium
10.45 - 11.15	Solutions, Opportunities & Innovation in Automotive Applications Andre Blum, Project Manager, Audi
11.15 - 11.45	Ready for Flight with FD-SOI Olivier Notebaert, Expert, Airbus
11.45 - 12.15	Invited Speech Kenichi Nakano, GM, Sony

12.15 - 01.15p.m. **LUNCH**

AFTERNOON SESSIONS

01.15 - 03.00p.m.	ECOSYSTEM IN EVOLUTION Chair: G. Cesana, SOI Industry Consortium
01.15- 01.45	FD-SOI & finFET: And gate? Or gate? How Decision Maker and Influencer Views Changed Since 2016 Dan Hutcheson, CEO, VLSIResearch
01.45 - 02.10	Invited Speech Hong Hao, SVP Foundry Business, Samsung

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AFTERNOON SESSIONS

Continues —

01.15 - 03.00p.m. ECOSYSTEM IN EVOLUTION

02.10 - 02.35 **FDX - Market Validated Differentiation**
Bami Bastani, SVP, Globalfoundries

02.35 - 03.00 **China's Strategy to Promote IC Industry**
Jeffrey Wang, CEO, Simgui

03.00 - 03.30p.m. NETWORKING BREAK

03.30 - 04.45p.m. INNOVATIVE APPLICATIONS
Chair: G. Cesana, SOI Industry Consortium

03.30 - 03.45 **FDSOI for IoT & Automotive**
Ramkumar Subramanian, SVP, Ineda Systems

03.45 - 04.00 **AI Applications at the Very Edge**
Loic Lietar, CEO, Greenwaves

04.00 - 04.15 **Automotive ADAS MPSoC Roadmap on 22nm FDSOI Technology**
Jens Benndorf, CEO, Dreamchip

04.15 - 04.30 **Enabling Data-intensive IoT Applications Thanks to FD-SOI and eMRAM**
Jean Pascal Bost, CEO, Evaderis

04.30 - 04.45 **Realizing Near-Threshold Computing with 22FDX & Low Power Memories**
William Koven, CEO, Reduced Energy Microsystems

04.55 - 05.55p.m. PANEL -
Needs for Ultra Low Power Electronics and its Application Drivers

Moderator: Giorgio Cesana, SOI Industry Consortium

Panelists:

Mahesh Tirupattur, EVP, Analog Bits

Kelvin Low, VP, ARM

Dave Eggleston, VP, Globalfoundries

Tim Saxe, CTO, Quicklogic

Samir Patel, CEO, Sankalp Semiconductor

Wayne Dai, CEO, Verisilicon

05.55 - 06.00p.m. CLOSING REMARKS
Giorgio Cesana, Executive Director, SOI Industry Consortium