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Presentation Details	
Title	Automotive ADAS-Applications derived from the Multi-Processor SoC Platform taped out in 22nm FDSOI
Keywords	ADAS, SoC, MPSoC, CNN, FD-SOI, Video, Imaging, Automotive
Short elaboration of the subject	<p>The presentation explores the European collaboration on a 22nm System-on-Chip development for Advanced Driver Assistance System (ADAS). It will highlight partner co-operation and key trade-offs necessary to deliver the target architecture and performance. The scope includes:</p> <ul style="list-style-type: none"> - Project setup and target applications in the automotive market - Chip architecture and performance achievements, e.g. for Convolutional Neural Networks (CNN) - Physical Chip and reference platform for evaluation - Applications and Derivatives
Speaker bio (<100 words)	<p>Jens Benndorf has a PhD. from Moscow University (1991) where he researched DSP technologies for use in high speed voice modems. Jens' passion for DSPs continued within international blue chip companies including Alcatel, BridgeCo, SciWorx and Silicon Image. He built and led teams developing products for early DSL and complex imaging applications.</p> <p>In 2010 Jens cofounded DreamChip Technologies GmbH and now leads the company in his role as MD and COO. DreamChip specialises in system on complex MPSoC designs for imaging applications.</p> <p>The company is based in Hannover, Germany and is a member of the Silicon Saxony network.</p>

	http://www.dreamchip.de/